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PATENT  
[D8143-00327]

ATTORNEY DOCKET: BOCCUZZI 2-27-3

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Boccuzzi et al.

Serial No.: Not assigned

Filed: Herewith

For: Differently-Tuned VCO Using Inductively Coupled Varactors

Certification under 37 CFR § 1.10

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I hereby certify that this correspondence and the documents referred to as attached therein are being deposited with the U.S. Postal Service in an envelope as "EXPRESS MAIL POST OFFICE TO ADDRESSEE" service under 37 CFR § 1.10, Mailing Label Number as listed above, addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date April 1, 2004

By Mary La Grange

M.S. Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER**

Enclosed herewith is an Information Disclosure Statement pursuant to 37 CFR. § 1.56 in connection with the above-identified application, which statement is being filed:

☒ [XX] Together with the present application.

☐ [ ] Before the first Office Action on the merits or three (3) months from the filing date of this application, whichever occurs last. [37 CFR § 1.97(b)]

☐ [ ] After the first Office Action on the merits, but before a Final Office Action under §1.113 or Notice of Allowance under §1.311, whichever occurs first. [37 CFR §1.97(c)]. (Either the fee or certification is provided, as set forth below).

☐ After a Final Office Action under §1.113 or Notice of Allowance under §1.311, but prior to or with payment of the Issue Fee. *[37 CFR §1.97(d)]. The fee and certification are provided, as set forth below).*

Consistent with Applicant's obligations pursuant to 37 CFR §§1.97 and 1.98, the following requirements have been met:

☐ No separate requirements are needed.

☐ No additional fee is required.

**Fee Under 37 CFR § 1.97(c) or 1.97(d)**

☐ The fee of \$180.00 for submission of an IDS under § 1.97(c) as set forth in § 1.17(p) accompanies this statement.

**Certification Under 37 CFR § 1.97(e)**

The undersigned certifies:

☐ 1 that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement; or

☐ 2 that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

**Provision of Copies of References**

☐ A copy of each of the references listed on the attached Form PTO-1449 is enclosed herewith and forms a part hereof.

☐ A translation of Reference \_\_\_\_\_ is enclosed herewith and forms a part hereof.

☐ A copy of the European Search Report from a corresponding or related EPO application is enclosed herewith.

☐ A copy of the International Search Report from a corresponding or related PCT application is enclosed herewith.

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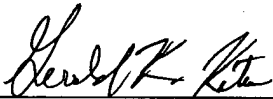
**Identification of Prior Application(s) In Which Listed Information Was Already Cited And For Which No Copies Are Submitted Or Need Be Submitted**

[ ] This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior U.S. Application No(s). , filed . The following references were submitted to, and/or cited by, the Office in the prior application(s) and therefore are not required to be provided in this application:

The Commissioner is hereby authorized to charge any fees associated with this communication or credit any overpayment to Deposit Account No. 04-1679.

Respectfully submitted,

Date: April 1, 2004

  
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Gerald K. Kita  
Registration No. 24,125

DUANE MORRIS LLP  
One Liberty Place  
Philadelphia, PA 19103-7396  
215-979-1863 tel  
215-979-1020 fax

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			<b>Complete if Known</b>		
			Application Number	Not assigned	
			Filing Date	Herewith	
			First Named Inventor	Boccuzzi, Vito	
			Art Unit	Not assigned	
			Examiner Name	Not assigned	
Sheet		of		Attorney Docket Number	BOCCUZZI 2-27-3

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code (If known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T
		Country Code - Number - Kind Code (if known)				

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the Item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T
	A	FONG, N. et al., "A 1-V 3.8-5.7-GHz Wide-Band VCO with Differentially Tuned Accumulation MOS Varactors for Common-Mode Noise Rejection in CMOS SOI Technology", IEEE Transactions on Microwave Theory and Techniques, Vol. 51, No. 8, August 2003, pp 1952-1059.	
	B	KAPUR and LONG, "IES: Efficient Electrostatic and Electromagnetic Simulation", IEEE Computational Science & Engineering, Oct-Dec 1998, pp 60-67	
	C	BEHBAHANI AND ABIDI, "RF-CMOS Oscillators with Switched Tuning", IEEE 1998 Custom Integrated Circuits Conference, pp 555 -558.	
	D	LEVANTINO et al., "Frequency Dependence on Bias Current in 5-GHz CMOS VCOs: Impact on Tuning Range and Flicker Noise Upconversion", IEEE Journal of Solid-State Circuits, Vol. 37, No. 8, August 2002, pp 1003-1011.	
	E	MAGIEROWSKI et al., "Differentially Tunable Varactor with Build-In Common-Mode Rejection", 2002 IEEE, pp I-559-I-562.	
	F	TIEBOUT, M., "Low-Power Low-Phase-Noise Differentially Tuned Quadrature VCO Design in Standard CMOS", IEEE Journal of Solid-State Circuits, Vol. 36, No. 7, July 2001, pp 1018-1024.	

Examiner Signature		Date Considered	
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